| Register | Number | Usage |
|---|---|---|
| zero | 0 | Hardwired 0 |
| at | 1 | Reserved for assembler |
| v0 | 2 | Function Return Value |
| v1 | 3 | Function Return Value |
| a0 | 4 | Argument 1 |
| a1 | 5 | Argument 2 |
| a2 | 6 | Argument 3 |
| a3 | 7 | Argument 4 |
| t0 | 8 | Temporary (Caller Save) |
| t1 | 9 | Temporary (Caller Save) |
| t2 | 10 | Temporary (Caller Save) |
| t3 | 11 | Temporary (Caller Save) |
| t4 | 12 | Temporary (Caller Save) |
| t5 | 13 | Temporary (Caller Save) |
| t6 | 14 | Temporary (Caller Save) |
| t7 | 15 | Temporary (Caller Save) |
| s0 | 16 | Saved Temporary (Callee Save) |
| s1 | 17 | Saved Temporary (Callee Save) |
| s2 | 18 | Saved Temporary (Callee Save) |
| s3 | 19 | Saved Temporary (Callee Save) |
| s4 | 20 | Saved Temporary (Callee Save) |
| s5 | 21 | Saved Temporary (Callee Save) |
| s6 | 22 | Saved Temporary (Callee Save) |
| s7 | 23 | Saved Temporary (Callee Save) |
| t8 | 24 | Temporary (Caller Save) |
| t9 | 25 | Temporary (Caller Save) |
| k0 | 26 | Reserved for OS kernel |
| k1 | 27 | Reserved for OS kernel |
| gp | 28 | Pointer to global area |
| sp | 29 | Stack Pointer |
| fp | 30 | Frame Pointer (callee save) |
| ra | 31 | Return Address (callee save) |

Figure 38: The MIPS register conventions